METHOD FOR MANUFACTURING THE SAME

Inventors: Jee-Soo Mok, Byung-Kook Sun, Chang-Kyu Song, Geum-Hee Yun, Tae-Hoon Kim

Song, Geum-Hee Yun, Tae-Hoon Kim Docket No.: LEPA122042

1/14 Fig. 1a PRIOR ART

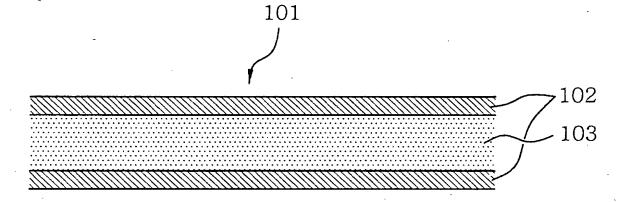
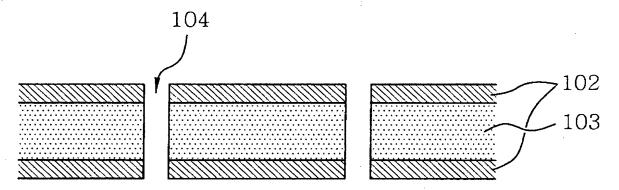


Fig. 1b PRIOR ART



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2/14
Fig. 1c
PRIOR ART

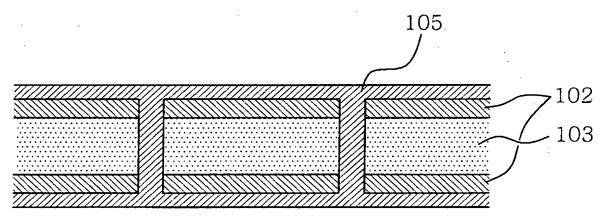
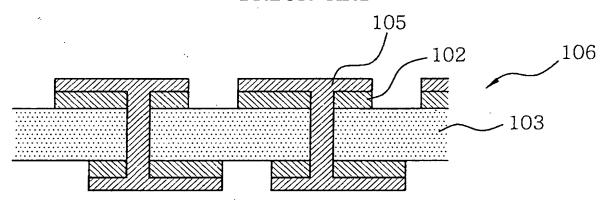


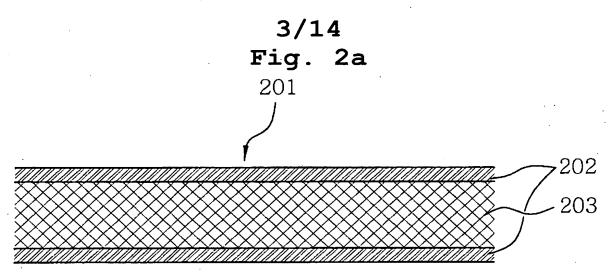
Fig. 1d PRIOR ART

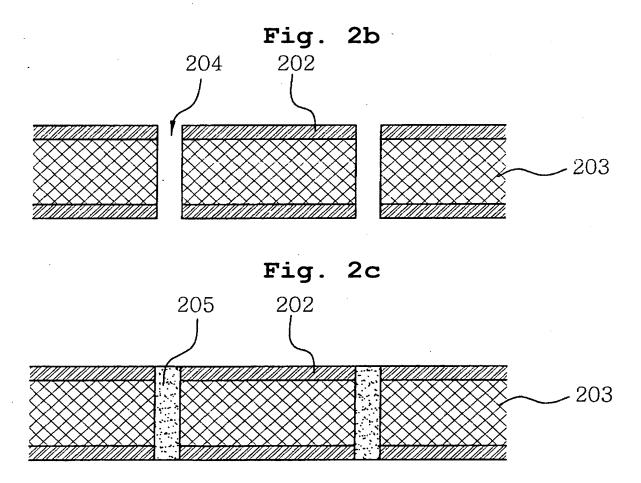


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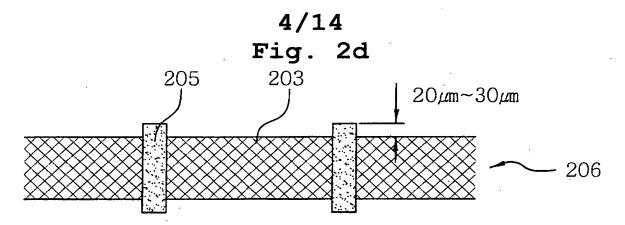


METHOD FOR MANUFACTURING THE SAME

Jee-Soo Mok, Byung-Kook Sun, Chang-Kyu Inventors:

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Docket No.: LEPA122042



Title: PARALLEL MULTI-LAYER PRINTED CIRCUIT BOARD HAVING IMPROVED INTERCONNECTION AND METHOD FOR MANUFACTURING THE SAME

Inventors:

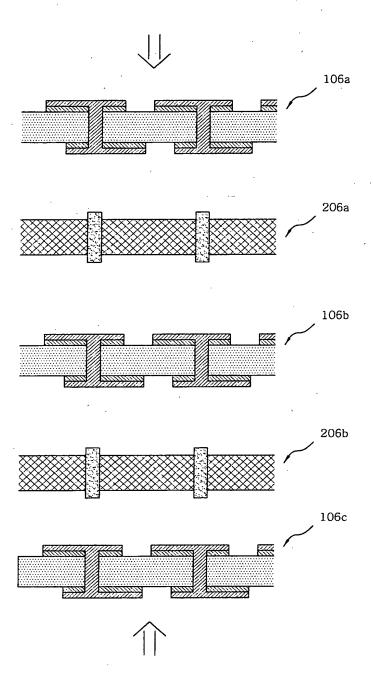
Jee-Soo Mok, Byung-Kook Sun, Chang-Kyu

Song, Geum-Hee Yun, Tae-Hoon Kim

Docket No.:

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5/14 Fig. 3 PRIOR ART



Title: PARALLEL MULTI-LAYER PRINTED CIRCUIT BOARD HAVING IMPROVED INTERCONNECTION AND METHOD FOR MANUFACTURING THE SAME

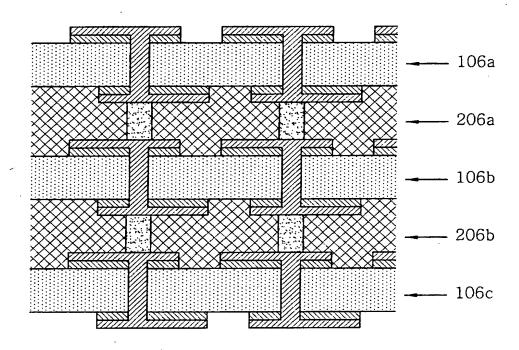
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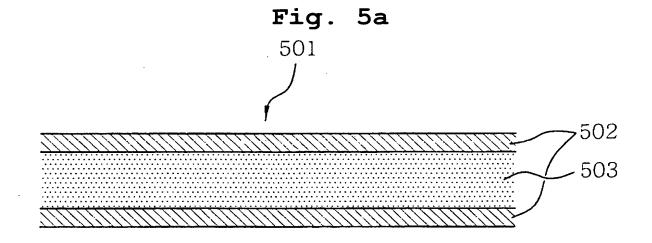
Jee-Soo Mok, Byung-Kook Sun, Chang-Kyu

Song, Geum-Hee Yun, Tae-Hoon Kim

Docket No.: LEPA122042

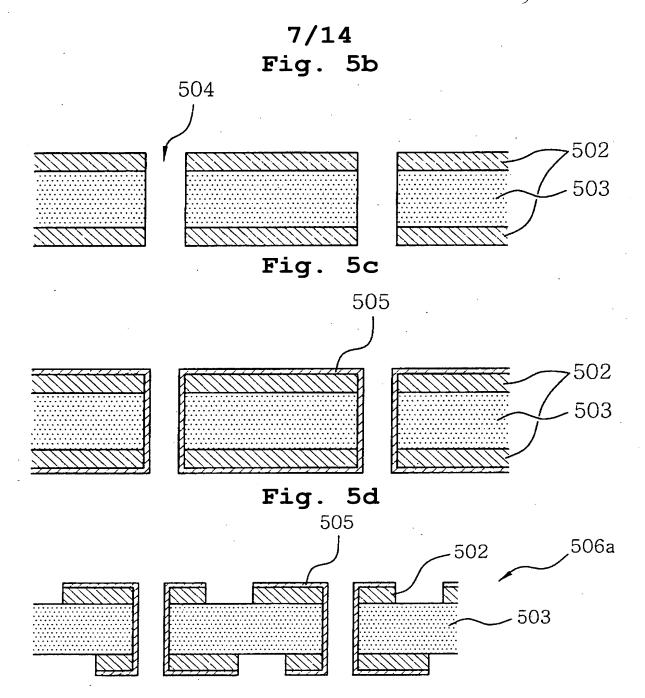
6/14 Fig. 4 PRIOR ART





METHOD FOR MANUFACTURING THE SAME

Inventors: Jee-Soo Mok, Byung-Kook Sun, Chang-Kyu Song, Geum-Hee Yun, Tae-Hoon Kim
Docket No.: LEPA122042

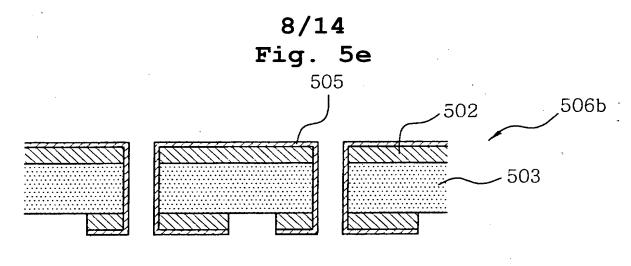


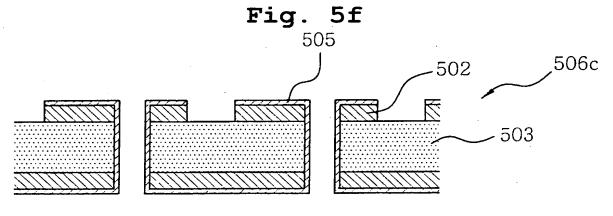
Title: PARALLEL MULTI-LAYER PRINTED CIRCUIT BOARD HAVING IMPROVED INTERCONNECTION AND METHOD FOR MANUFACTURING THE SAME

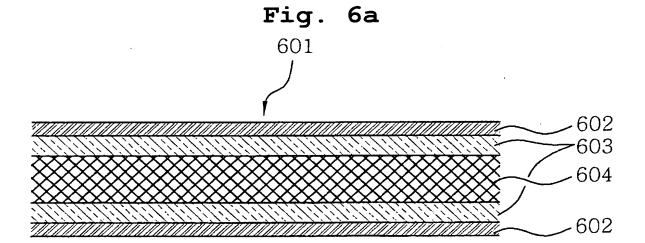
Jee-Soo Mok, Byung-Kook Sun, Chang-Kyu

Inventors:

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Title: PARALLEL MULTI-LAYER PRINTED CIRCUIT BOARD HAVING IMPROVED INTERCONNECTION AND METHOD FOR MANUFACTURING THE SAME

Jee-Soo Mok, Byung-Kook Sun, Chang-Kyu

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9/14 Fig. 6b

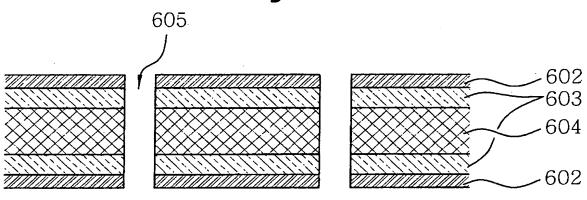


Fig. 6c

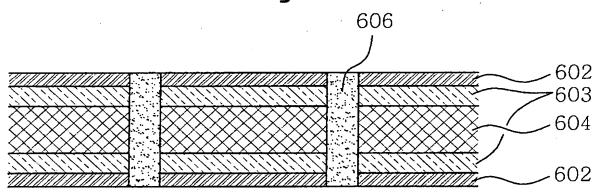


Fig. 6d 606 38µm 607 603 604

METHOD FOR MANUFACTURING THE SAME

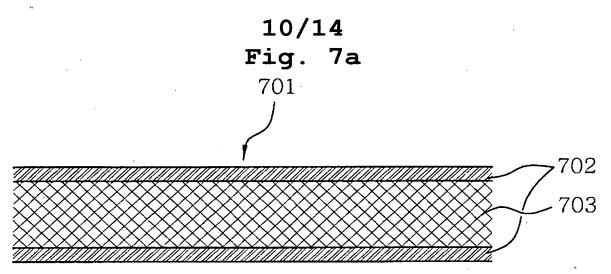
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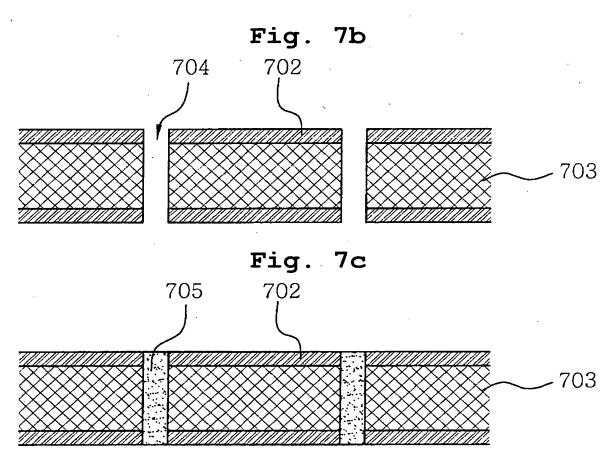
5

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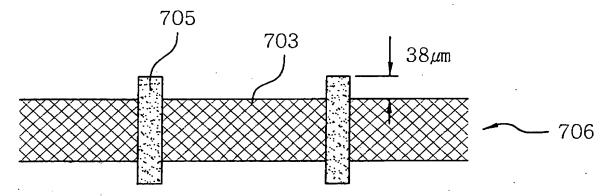


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11/14 Fig. 7d

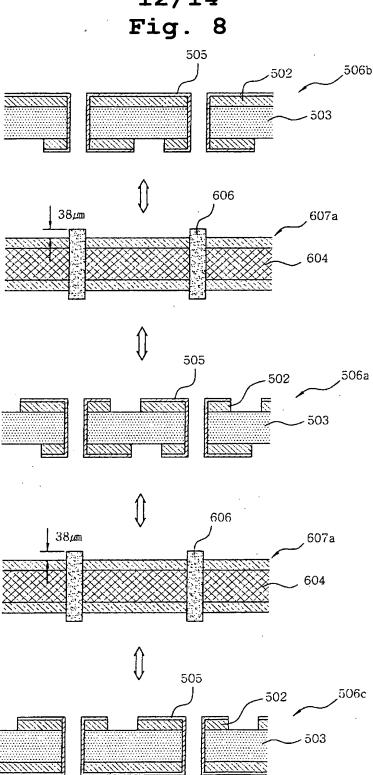


Title: PARALLEL MULTI-LAYER PRINTED CIRCUIT BOARD HAVING IMPROVED INTERCONNECTION AND METHOD FOR MANUFACTURING THE SAME

Jee-Soo Mok, Byung-Kook Sun, Chang-Kyu

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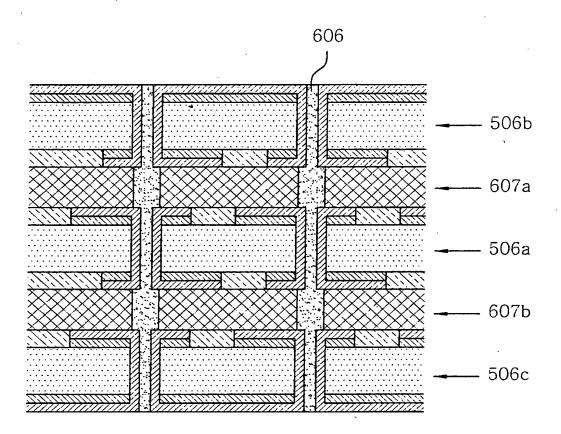


Title: PARALLEL MULTI-LAYER PRINTED CIRCUIT BOARD HAVING IMPROVED INTERCONNECTION AND METHOD FOR MANUFACTURING THE SAME

Jee-Soo Mok, Byung-Kook Sun, Chang-Kyu

Song, Geum-Hee Yun, Tae-Hoon Kim LEPA122042 Docket No.:

13/14 Fig. 9



METHOD FOR MANUFACTURING THE SAME

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> 14/14 Fig. 10

